

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	1165	(card or cartridge) same cooling and connector	USPA T	2004/08/19 12:27			0
2	BRS	L2	306	1 and @py<1993	USPA T	2004/08/19 12:27			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	IS&R	L15	6	((("6608753") or ("6771509") or ("6515864") or ("6404639") or ("6025993") or ("5659459")) .PN.	USPA T	2004/08/19 10:43			0
2	IS&R	L16	62	((("2164803") or ("0198378") or ("3416348") or ("3237878") or ("6515864") or ("6515823") or ("6404639") or ("6025993") or ("5659459") or ("5615085") or ("5548487") or ("5526229") or ("5504669") or ("5481656") or ("5475563") or ("5473506") or ("5437041") or ("5432913") or ("5424913") or ("5389819") or ("5289342") or ("5287247") or ("5262922") or ("5227631") or ("5208732") or ("5198965") or ("5195176") or ("5184211") or ("5162610") or ("5142614") or ("5138521") or ("5136464") or	USPA T	2004/08/19 10:43			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
3	IS&R	L17	44	((("6515864") or ("6404639") or ("6025993") or ("5659459") or ("5615085") or ("5548487") or ("5526229") or ("5504669") or ("5481656") or ("5475563") or ("5473506") or ("5437041") or ("5432913") or ("5424913") or ("5289342") or ("5287247") or ("5227631") or ("5208732") or ("5198965") or ("5195176") or ("5162610") or ("5142614") or ("5136464") or ("5136146") or ("5060112") or ("5031076") or ("5024264") or ("5016086") or ("4962444") or ("4914551") or ("4872091") or ("4849944") or	USPA T	2004/08/19 10:44			0
4	IS&R	L18	3	((("5615085") or ("5526229") or ("5437041"))).PN.	USPA T	2004/08/19 10:49			0
5	IS&R	L19	1	("3225262").PN.	USPA T	2004/08/19 10:50			0
6	IS&R	L20	1	("4855869").PN.	USPA T	2004/08/19 10:50			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
7	BRS	L21	3585	((circuit adj board) or substrate) same (cooling same (compress\$3 or resilient or foam or rubber or silicon))	USPA T	2004/08/19 10:59			0
8	BRS	L22	89	21 and (CPU or processor or microprocessor or ic) same (heat adj sink)	USPA T	2004/08/19 11:00			0
9	BRS	L23	1293	((circuit adj board) or substrate) same (cooling same (compress\$3 or resilient or foam or rubber or silicon))	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/19 10:57			0
10	BRS	L24	9	23 and (CPU or processor or microprocessor or ic) same (heat adj sink)	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/19 10:57			0
11	BRS	L25	20003	((circuit adj board) or substrate) same (support\$3 same (compress\$3 or resilient or foam or rubber or silicon))	USPA T	2004/08/19 11:03			0
12	BRS	L26	112	25 and (CPU or processor or microprocessor or ic) same (heat adj sink)	USPA T	2004/08/19 11:03			0
13	BRS	L27	83	26 not 22	USPA T	2004/08/19 11:00			0

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
14 BRS	L28	13704	((circuit adj board) or substrate) same (support\$3 same (compress\$3 or resilient or foam or rubber or silicon))	US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/19 11:05			0
15 BRS	L29	4	28 and (CPU or processor or microprocessor or ic) same (heat adj sink)	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/19 11:06			0
16 BRS	L30	10877	((circuit adj board) or substrate) same (bottom same (compress\$3 or resilient or foam or rubber or silicon))	US-P GPUB ; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/19 11:06			0
17 BRS	L31	3	30 and (CPU or processor or microprocessor or ic) same (heat adj sink)	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/19 11:05			0
18 BRS	L32	16598	((circuit adj board) or substrate) same (bottom same (compress\$3 or resilient or foam or rubber or silicon))	USPA T	2004/08/19 11:06			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
19	BRS	L33	117	32 and (CPU or processor or microprocessor or ic) same (heat adj sink)	USPA T	2004/08/19 11:06			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	IS&R	L1	6	((("6771509") or ("6608753") or ("6515864") or ("6404639") or ("6025993") or ("5659459")).PN.	USPA T	2004/08/18 13:51			0
2	BRS	L2	10372	((circuit adj board) or substrate) and heat and cooling and spring\$1	USPA T	2004/08/18 13:52			0
3	BRS	L3	8678	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	USPA T	2004/08/18 13:53			0
4	BRS	L4	529	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	USPA T	2004/08/18 14:28			0
5	BRS	L5	214541	4 and 5-20-92 to present	USPA T	2004/08/18 13:58			0
6	BRS	L6	529	4 ( 5-20-92 to present).py.	USPA T	2004/08/18 14:06			0
7	BRS	L7	0	4 and @pd=19920520	USPA T	2004/08/18 14:08			0
8	BRS	L8	128	4 and @py<1992	USPA T	2004/08/18 14:08			0
9	BRS	L9	138	4 and @py<1993	USPA T	2004/08/18 14:28			0
10	BRS	L11	0	10 and @py<1993	USPA T	2004/08/18 14:17			0
11	BRS	L10	289	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:23			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
12	BRS	L12	612	((circuit adj board) or substrate) same (heat adj sink) same (pressed or compressed or resilient)	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:24			0
13	BRS	L13	452	12 not 6	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:24			0
14	BRS	L14	71	13 and @py<1993	USPA T	2004/08/18 14:24			0
15	BRS	L15	1152	((circuit adj board) or substrate) and (heat adj sink) same spring\$1	USPA T	2004/08/18 14:51			0
16	BRS	L16	623	15 not 4	USPA T	2004/08/18 14:28			0
17	BRS	L17	180	16 and @py<1993	USPA T	2004/08/18 14:38			0
18	BRS	L18	362	((circuit adj board) or substrate) and (heat adj sink) same spring\$1	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:38			0
19	BRS	L19	0	18 and @py<1993	USPA T	2004/08/18 14:38			0



	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
20	BRS	L20	136	18 and @py<1993	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:46			0
21	BRS	L21	175	((circuit adj board) or substrate) same processor same spring\$1 same spring\$1	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:46			0
22	BRS	L22	175	((circuit adj board) or substrate) same processor same spring\$1	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:49			0
23	BRS	L23	36	22 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:50			0
24	BRS	L24	51	((circuit adj board) or substrate) same processor same (resilient or compressible)	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:49			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
25	BRS	L25	9	24 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:51			0
26	BRS	L26	3883	((circuit adj board) or substrate) and (heat adj sink) and spring\$1	USPA T	2004/08/18 15:24			0
27	BRS	L27	1076	26 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 15:16			0
28	BRS	L28	896	27 not 17	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:52			0
29	BRS	L29	896	28 not 20	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/18 14:53			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
30	BRS	L30	427	((circuit adj board) or substrate) and (heat adj sink) and spring\$1	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:16			0
31	BRS	L31	167	30 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:22			0
32	BRS	L32	622	361/704.ccls. and spring\$1	USPA T	2004/08/1 8 15:22			0
33	BRS	L33	56	32 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:25			0
34	BRS	L34	13437	((circuit adj board) or substrate) and cooling and spring\$1	USPA T	2004/08/1 8 15:24			0
35	BRS	L35	1143234	not 26	USPA T	2004/08/1 8 15:24			0
36	BRS	L36	3137	35 and @py<1993	USPA T	2004/08/1 8 15:25			0
37	BRS	L37	259	36 and processor	USPA T	2004/08/1 8 15:26			0

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
1	IS&R	L1	6	((("6771509") or ("6608753") or ("6515864") or ("6404639") or ("6025993") or ("5659459")).PN.	USPA T	2004/08/1 8 13:51			0
2	BRS	L2	10372	((circuit adj board) or substrate) and heat and cooling and spring\$1	USPA T	2004/08/1 8 13:52			0
3	BRS	L3	8678	((circuit adj board) or substrate) same (heat adj sink) same\$pring\$1	USPA T	2004/08/1 8 13:53			0
4	BRS	L4	529	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	USPA T	2004/08/1 8 15:44			0
5	BRS	L5	21454 1	4 and 5-20-92 to present	USPA T	2004/08/1 8 13:58			0
6	BRS	L6	529	4 ( 5-20-92 to present).py.	USPA T	2004/08/1 8 14:06			0
7	BRS	L7	0	4 and @pd=19920520	USPA T	2004/08/1 8 14:08			0
8	BRS	L8	128	4 and @py<1992	USPA T	2004/08/1 8 14:08			0
9	BRS	L9	138	4 and @py<1993	USPA T	2004/08/1 8 14:28			0
10	BRS	L11	0	10 and @py<1993	USPA T	2004/08/1 8 14:17			0
11	BRS	L10	289	((circuit adj board) or substrate) same (heat adj sink) same spring\$1	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:23			0

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
12	BRS	L12	612	((circuit adj board) or substrate) same (heat adj sink) same (pressed or compressed or resilient)	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:24			0
13	BRS	L13	452	12 not 6	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:24			0
14	BRS	L14	71	13 and @py<1993	USPA T	2004/08/1 8 14:24			0
15	BRS	L15	1152	((circuit adj board) or substrate) and (heat adj sink) same spring\$1	USPA T	2004/08/1 8 14:51			0
16	BRS	L16	623	15 not 4	USPA T	2004/08/1 8 14:28			0
17	BRS	L17	180	16 and @py<1993	USPA T	2004/08/1 8 14:38			0
18	BRS	L18	362	((circuit adj board) or substrate) and (heat adj sink) same spring\$1	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:38			0
19	BRS	L19	0	18 and @py<1993	USPA T	2004/08/1 8 14:38			0

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Error Counts
20	BRS	L20	136	18 and @py<1993	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:46			0
21	BRS	L21	175	((circuit adj board) or substrate) same processor same spring\$1 same spring\$1	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:46			0
22	BRS	L22	175	((circuit adj board) or substrate) same processor same spring\$1	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:49			0
23	BRS	L23	36	22 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:50			0
24	BRS	L24	51	((circuit adj board) or substrate) same processor same (resilient or compressible)	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:49			0

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Error Counts
25	BRS	L25	9	24 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:51			0
26	BRS	L26	3883	((circuit adj board) or substrate) and (heat adj sink) and spring\$1	USPA T	2004/08/1 8 15:24			0
27	BRS	L27	1076	26 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:16			0
28	BRS	L28	896	27 not 17	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:52			0
29	BRS	L29	896	28 not 20	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 14:53			0

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Error Counts
30	BRS	L30	427	((circuit adj board) or substrate) and (heat adj sink) and spring\$1	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:16			0
31	BRS	L31	167	30 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:22			0
32	BRS	L32	622	361/704.ccls. and spring\$1	USPA T	2004/08/1 8 15:22			0
33	BRS	L33	56	32 and @py<1993	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:25			0
34	BRS	L34	13437	((circuit adj board) or substrate) and cooling and spring\$1	USPA T	2004/08/1 8 15:24			0
35	BRS	L35	11432	34 not 26	USPA T	2004/08/1 8 15:24			0
36	BRS	L36	3137	35 and @py<1993	USPA T	2004/08/1 8 15:25			0
37	BRS	L37	259	36 and processor	USPA T	2004/08/1 8 15:26			0
38	BRS	L38	9	((circuit adj board) or substrate) same cooling same foam same housing	USPA T	2004/08/1 8 15:50			0



	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
39	BRS	L39	2	((circuit adj board) or substrate) same cooling same foam same housing	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:46			0
40	BRS	L40	9	((circuit adj board) or substrate) same cooling same (compressible or compressed) same housing	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:48			0
41	BRS	L41	6	((circuit adj board) or substrate) same cooling same (compressible or compressed) same housing	USPA T	2004/08/1 8 15:48			0
42	BRS	L42	0	((circuit adj board) or substrate) same cooling same (silicon same rubber) same housing	USPA T	2004/08/1 8 15:50			0
43	BRS	L43	0	((circuit adj board) or substrate) same cooling same (silicon same rubber) same housing	USPA T	2004/08/1 8 15:51			0
44	BRS	L44	24	((circuit adj board) or substrate) same cooling same (rubber) same housing	USPA T	2004/08/1 8 15:52			0
45	BRS	L45	12	((circuit adj board) or substrate) same between same compressible same processor	USPA T	2004/08/1 8 15:54			0

	Type	L #	Hits	Search Text	Dbs	Time Stamp	Comments	Error Definition	Errors
46	BRS	L46	1	((circuit adj board) or substrate) same between same compressible same processor	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:55			0
47	BRS	L47	3	((circuit adj board) or substrate) same between same resilient same processor	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 15:55			0
48	BRS	L48	13	((circuit adj board) or substrate) same between same resilient same processor	USPA T	2004/08/1 8 15:56			0
49	BRS	L49	62	((circuit adj board) or substrate) same between same spring same processor	USPA T	2004/08/1 8 15:58			0
50	BRS	L50	18	((circuit adj board) or substrate) same between same spring same processor	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 16:00			0
51	BRS	L51	5	((circuit adj board) or substrate) same support same spring same processor	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 16:01			0
52	BRS	L52	1	((circuit adj board) or substrate) same support same (rubber or foam) same processor	EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 16:02			0

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
53	BRS	L53	21	((circuit adj board) or substrate) same support same springs same processor	USPA T; EPO; JPO; DERW ENT; IBM_ TDB	2004/08/1 8 16:03			0